

FORM PTO-1449
(Rev. 2-32)

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Atty. Docket No.

Serial No.

01-1205-A

10/658,710

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

(Use several sheets if necessary)

Applicant: Elad Alon, et al.

Filing Date:
09/08/2003

Group:

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
AL	5,614,855	03-1997	Lee et al	327	158	8/21/95
AL	5,945,862	08-1999	Donnelly et al.	327	278	7/31/97
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AL	6,125,157	09-2000	Donnelly et al.	375	371	2/6/97
AL	6,133,773	10-2000	Garlepp et al.	327	247	10/10/97
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AL	6,147,531	11-2000	McCall et al.	327	158	7/9/98
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FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc).

HL		HL	Jung-Bae Lee, et al., Digitally-Controlled DLL, and I/O Circuit for 500Mb/s/pin x16 DDR SDRAM, ISSCC 2001, Session 4 High-Speed Digital Interfaces, 4.6, February 5, 2001, 2 pages
EXAMINER		DATE CONSIDERED	
LUU		3/31/04	

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